



Figure similar

*** Spare part *** SIMATIC DP, Electronics module for ET 200S, 2 AO I High Speed 15 mm width, +/-20mA; 16 bit, 4..20 mA; 16 bit, Cycle time 0.1 ms with SF LED (group fault)

General information	
Product function	
<ul style="list-style-type: none"> • Isochronous mode 	Yes
Supply voltage	
Load voltage L+	
<ul style="list-style-type: none"> • Rated value (DC) 	24 V
<ul style="list-style-type: none"> • Reverse polarity protection 	Yes
Input current	
from load voltage L+ (without load), max.	150 mA; With load
from backplane bus 3.3 V DC, max.	10 mA
Power loss	
Power loss, typ.	2.4 W
Address area	
Address space per module	
<ul style="list-style-type: none"> • Address space per module, max. 	4 byte
Analog outputs	
Number of analog outputs	2
Current output, no-load voltage, max.	18 V
Cycle time (all channels) max.	250 μs
Output ranges, current	
<ul style="list-style-type: none"> • -20 mA to +20 mA 	Yes
<ul style="list-style-type: none"> • 4 mA to 20 mA 	Yes
Connection of actuators	
<ul style="list-style-type: none"> • for current output two-wire connection 	Yes
<ul style="list-style-type: none"> • for current output four-wire connection 	No
Load impedance (in rated range of output)	
<ul style="list-style-type: none"> • with current outputs, max. 	500 Ω
<ul style="list-style-type: none"> • with current outputs, inductive load, max. 	1 mH; for TWA 100μs
Destruction limits against externally applied voltages and currents	
<ul style="list-style-type: none"> • Voltages at the outputs towards MANA 	15 V; Max. 15 V / 5 hours (higher voltages not permissible even briefly)
<ul style="list-style-type: none"> • Current, max. 	30 mA; DC
Cable length	
<ul style="list-style-type: none"> • shielded, max. 	200 m; Max. 20 m for TWA 100 μs
Analog value generation for the outputs	
Integration and conversion time/resolution per channel	
<ul style="list-style-type: none"> • Resolution with overrange (bit including sign), max. 	16 bit
Settling time	
<ul style="list-style-type: none"> • for resistive load 	0.05 ms
<ul style="list-style-type: none"> • for capacitive load 	0.05 ms; At a load of up to 500 Ω / 100 nF and a max. cable length of 20 m

• for inductive load	0.05 ms
Errors/accuracies	
Operational error limit in overall temperature range	
• Current, relative to output range, (+/-)	0.2 %; Specified value applies to loads from 200 to 350 Ohm, deviating operational limits for loads up to 200 Ohm and from 350 to 500 Ohm with up to 0.4%
Basic error limit (operational limit at 25 °C)	
• Current, relative to output range, (+/-)	0.1 %; Specified value applies for loads from 200 to 350 ohms, deviating basic error limits for loads up to 200 ohms and from 350 to 500 ohms with up to 0.3%
Interrupts/diagnostics/status information	
Diagnoses	
• Diagnostic information readable	Yes
• Wire-break	Yes
• Group error	Yes
Diagnostics indication LED	
• Group error SF (red)	Yes
Parameter	
Remark	7 byte
Diagnostics wire break	Disable / enable
Group diagnostics	Disable / enable
Response to CPU/master STOP	Output current and de-energized/substitute a value/keep last value
Potential separation	
Potential separation analog outputs	
• between the channels	No
• between the channels and backplane bus	Yes
• Between the channels and load voltage L+	Yes
Isolation	
Isolation tested with	500 V DC
Dimensions	
Width	15 mm
Height	81 mm
Depth	52 mm
Weights	
Weight, approx.	40 g

last modified: 5/22/2024 